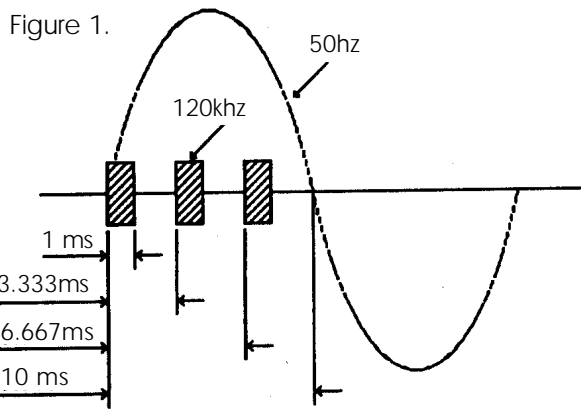


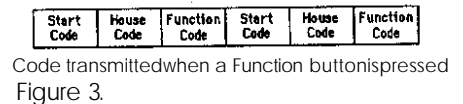
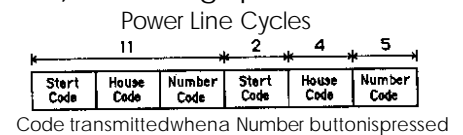
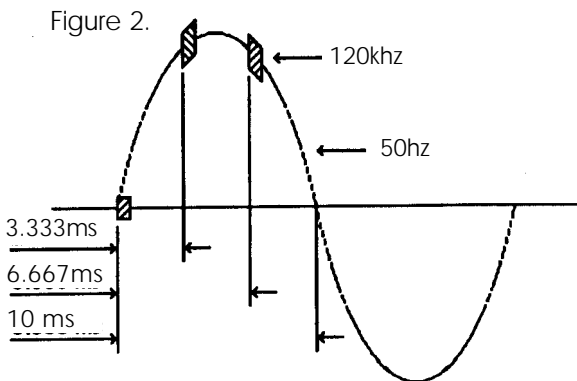
X-10 PRO transmissions are synchronized to the zero crossing point of the AC power line. The design goal should be to transmit as close to the zero crossing point as possible but certainly within 200 microseconds of the zero crossing point. The PSC04 and PSC05 provide a 50 Hz. square wave with a max. delay of 100 microseconds from the zero crossing point of the AC power line. The maximum delay between signal envelope input and 120KHz. output bursts is 50 microseconds. Therefore, it should be arranged that outputs to the PSC04 and PSC05 be within 50 microseconds of this 50 Hz. zero crossing reference square wave.

A Binary 1 is represented by a 1 millisecond burst of 120 KHz. at the zero crossing point and a Binary 0 by the absence of 120 KHz. The XM10 modulate its input(from the OEM) with 120KHz., therefore only the 1 ms "envelope" need be applied to their inputs. These 1 millisecond bursts should actually be transmitted three times to coincide with the zero crossing points of all three phases in a three phase distribution system. Figure 1 shows the timing relationship of these bursts relative to zero crossing.



Note. - For clarity, the signals in figure 1 are shown as they would be seen through a high pass filter. The 50 Hz. waveform is only shown for reference. In reality the signals are actually superimposed on the 50 Hz. waveform and look more like that shown in figure 2.

A complete code transmission encompasses eleven cycles of the power line. The first two cycles represent a Start Code. The next four cycles represent the House Code and the last five cycles represent either a Number Code (1 thru 16) or a Function Code (On, Off etc.). This complete block, (Start Code, House Code, Key Code) should always be transmitted in groups of 2 with 3 power line cycles between each group of 2 codes. Bright and dim are exceptions to this rule and should be transmitted continuously (at least twice) with NO gaps between codes. See figure 3.



Within each block of data, each four or five bit code should be transmitted in true and complement form on alternate half cycles of the power line. i.e. if a 1 millisecond burst of signal is transmitted on one half cycle, (binary 1) then no signal should be transmitted on the next half cycle, (binary 0). See Figure 4 below.

